



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application of:  
Shunpei YAMAZAKI et al.  
Serial No. 09/255,777  
Filed: February 23, 1998  
For: SEMICONDUCTOR DEVICE AND METHOD FOR  
FORMING THE SAME

Examiner: R. Booth  
Group Art Unit: 2812

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**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, DC 20231, on November 8, 2002.

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
Washington, DC 20231

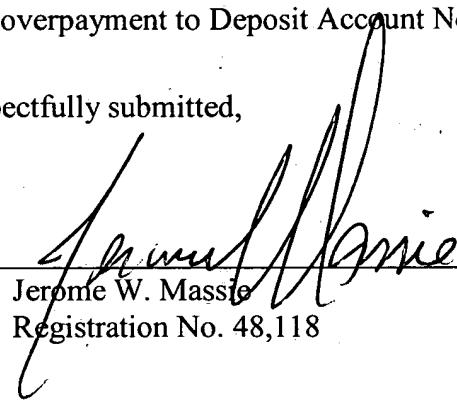
Sir:

In accordance with the duty of disclosure as set forth in 37 C.F.R. §1.56, Applicants hereby submit the following information in conformance with 37 C.F.R. §§ 1.97 and 1.98. Pursuant to 37 C.F.R. § 1.98, a copy of each of the documents cited is enclosed.

It is requested that the accompanying PTO-1449 be considered and made of record in the above-identified application. To assist the Examiner, the documents are listed on the attached form PTO-1449. It is respectfully requested that an Examiner initialed copy of this form be returned to the undersigned.

The Commissioner is hereby authorized to charge any fees connected with this filing which may be required now, or credit any overpayment to Deposit Account No. 19-2380.

Respectfully submitted,

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